KARATE CHAMP™

PLAYER vs PLAYER

INSTALLATION INSTRUCTIONS

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Telex 172163 DATA EAST SNTA

WARNING

This equipment generates and uses radio frequency energy and if not installed and used properly, i.e., in strict accordance with the instructions manual, may cause harmful interference to radio communications. It has been tested and found to comply with the limits for a Class A computing device persuant to Subpart J of Part 15 of FCC Rules, which are designed to provide reasonable protection against such interference when operated in a commercial environment.

Operation of this equipment in a residential area is likely to cause interference in which case the user at his own expense will be required to take whatever measures may be required to correct the interference.

CAUTION

EMI Shield must be securely installed in order to protect against undesirable radio interference.

KARATE CHAMP

Player Vs Player

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GAME INSTRUCTIONS

1. TRAINING

– Use this session to learn and to practice the various moves. You can enter directly into game play by pressing either 1 or 2 player start button.
– Left Joystick controls player's direction of movement.
– Right Joystick controls player's choice of attack.
– Both must be used together to execute the different moves.

2. MATCH

– Maneuver YOUR PLAYER to fight against your opponent.
– Move joysticks to execute the various moves (see move chart).
– First fighter to score 2 points wins match.
– Player advances to next level for each match won.

3. BONUS INTERVAL

– After each match, play bonus rounds to score additional points.

LEVER INSTRUCTIONS

WHITE PLAYER

Left Lever \ Right Lever					
		Back Kick	Front Kick Middle Reverse Punch	Round Kick	Low Kick
	Withdraw Block	Back Kick	Back Round Kick	Upper Punch	Low Kick
	Forward	Change Direction Back Kick	Middle Lunge Punch Front Kick	Upper Lunge Punch Round Kick	Low Kick
	Jump	Jamping Back Kick	Jumping Side Kick	Back Ward Somersault	Forward Somersault
	Squat	Back Foot Sweep	Front Foot Sweep	Squating Reverse Lunge Punch	Front Foot Sweep

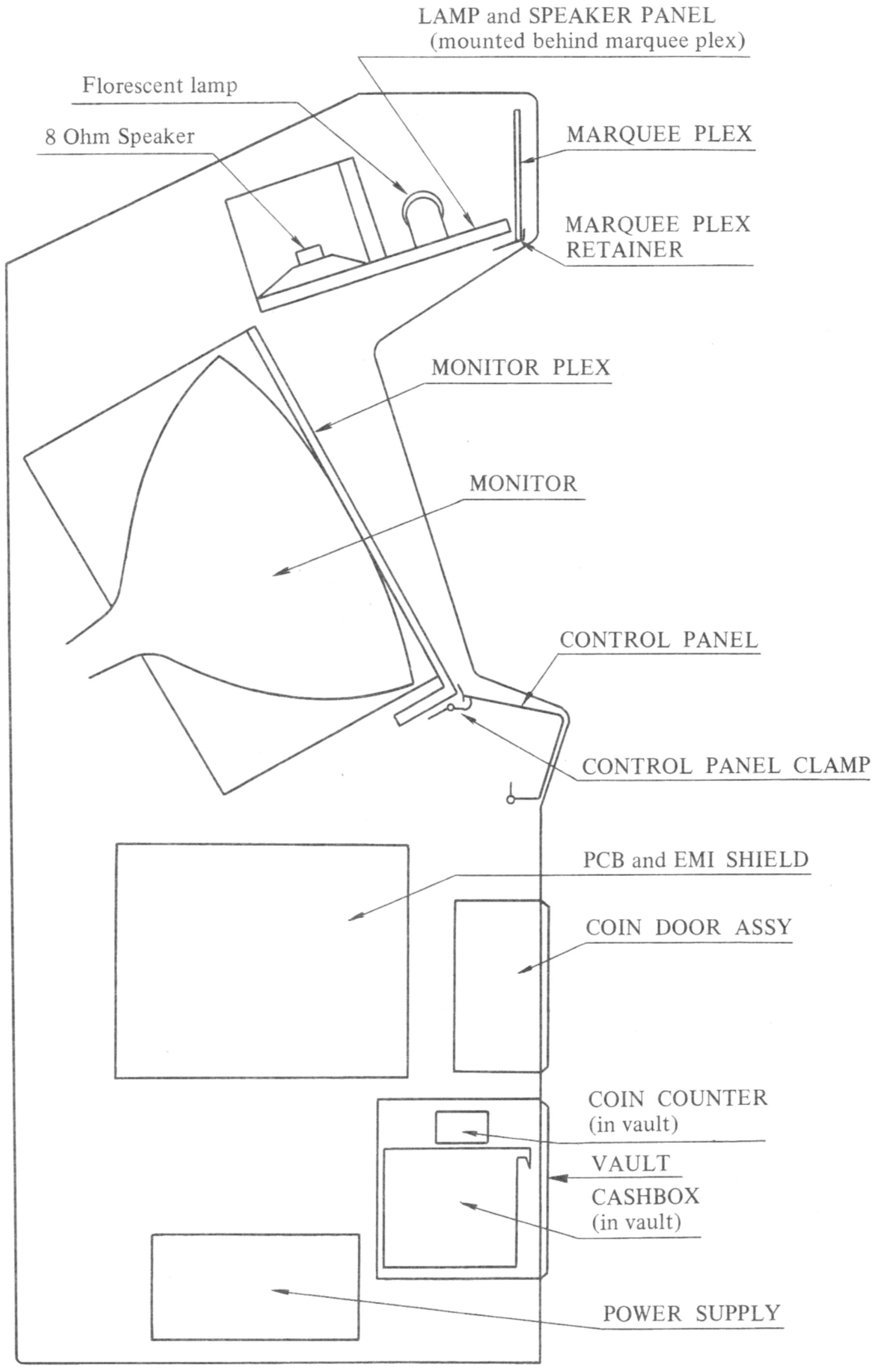
RED PLAYER

Left Lever \ Right Lever					
		Back Kick	Front Kick Middle Reverse Punch	Round Kick	Low Kick
	Withdraw Block	Back Kick	Back Round Kick	Upper Punch	Low Kick
	Forward	Change Direction Back Kick	Middle Lunge Punch Front Kick	Upper Lunge Punch Round Kick	Low Kick
	Jump	Jamping Back Kick	Jumping Side Kick	Back Ward Somersault	Forward Somersault
	Squat	Back Foot Sweep	Front Foot Sweep	Squatting Reverse Lunge Punch	Front Foot Sweep

OPTION SWITCH SETTINGS

1 2	OFF OFF	ON OFF	OFF ON	ON ON	right coin acceptor
	1 coin 1 play	1 coin 1 play	2 coin 1 play	3 coin 1 play	
3 4	OFF OFF	ON OFF	OFF ON	ON ON	left coin acceptor
	1 coin 1 play	1 coin 2 play	2 coin 1 play	3 coin 1 play	
5 6	OFF OFF	ON OFF	OFF ON	ON ON	DIFFICULTY SETTING
	EASIEST	HARD	HARDER	HARDER	
7	OFF		ON		Attract mode sound on?
	NO		YES		
8	OFF		ON		Mode
	NORMAL		FREE PLAY		

MAJOR CABINET COMPONENT LOCATIONS



POWER SUPPLY REQUIREMENTS

+5V	±2%	5A
+12V	±2%	1A

OPERATING ENVIRONMENT

	Temperature	Humidity
Operation	5 ~ 35°C	20 ~ 80%
Storage	−15 ~ 65°C	10 ~ 90%

RASTER SCAN MONITOR SIGNALS

(1) Color Signals R.G.B. Separate

Black	0 – +2V
Image Signal	+2.5V – +4V

(2) Syncronize Signal Composite

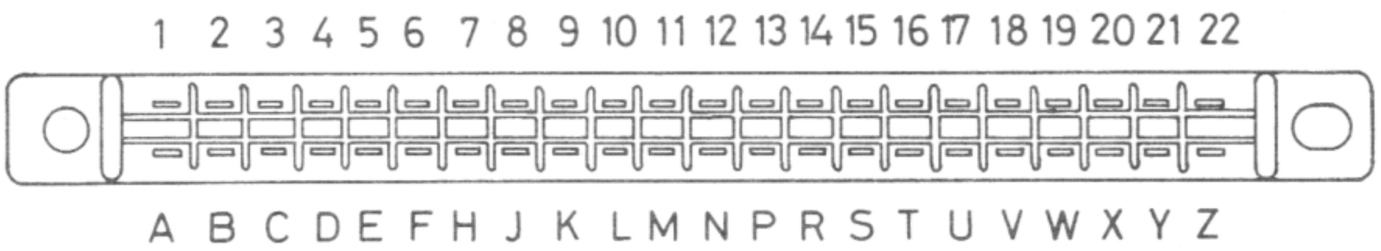
0	0 – 0.5V
1	+3V – +5V

(3) Screen Image Size

Image can be shrunk both horizontally and vertically by 10%.

Signal Assignment Table

Connector CR7E-44DA-3.96E (HIROSE) Terminal Numbering



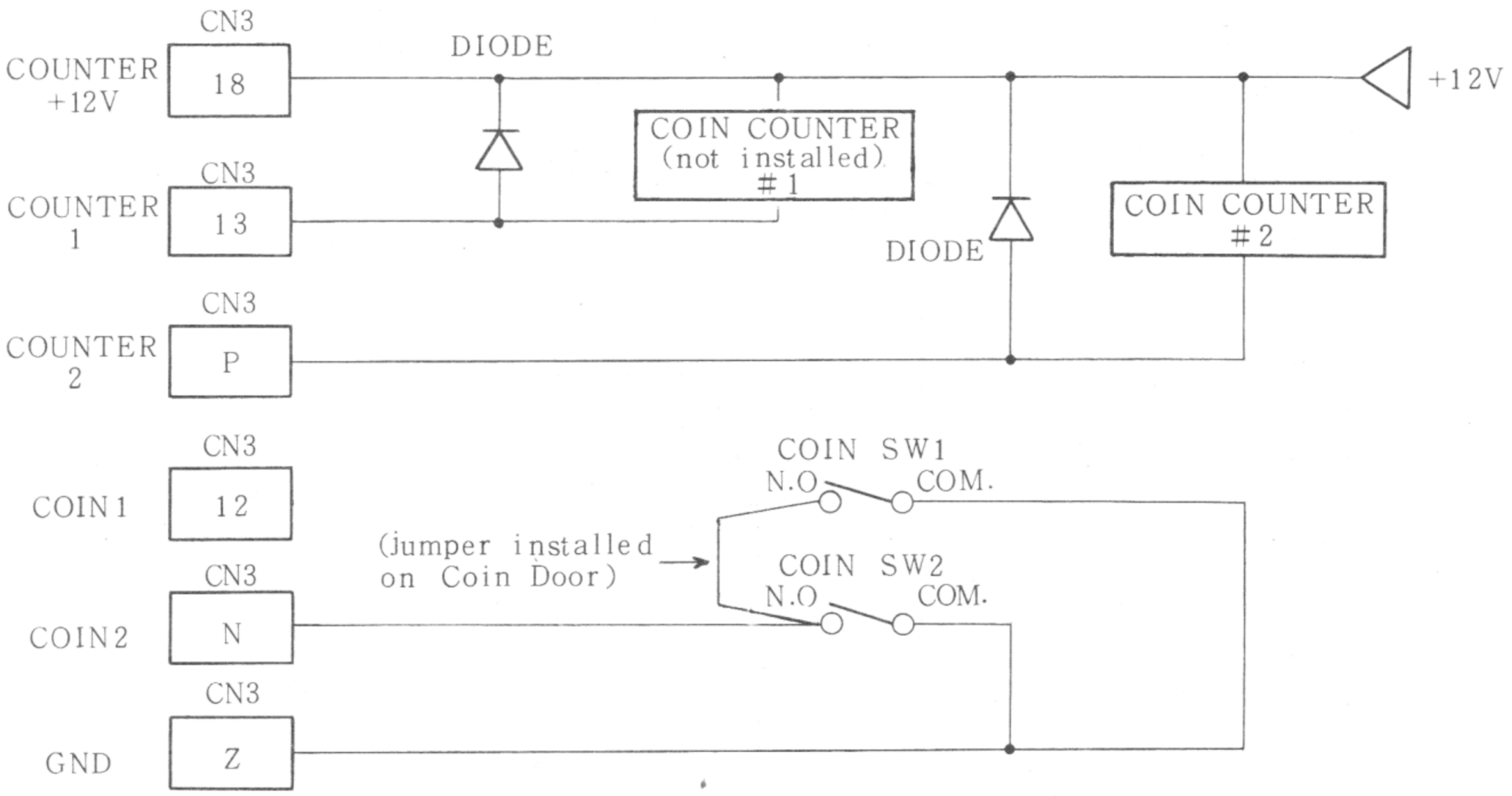
Signal Assignment

Pin No.	Signal	Pin No.	Signal
1	1P RIGHT DOWN	A	2P RIGHT DOWN
2	1P RIGHT UP	B	2P RIGHT UP
3	1P RIGHT LEFT	C	2P RIGHT LEFT
4	1P RIGHT RIGHT	D	2P RIGHT RIGHT
5	1P LEFT DOWN	E	2P LEFT DOWN
6	1P LEFT UP	F	2P LEFT UP
7	1P LEFT LEFT	H	2P LEFT LEFT
8	1P LEFT RIGHT	J	2P LEFT RIGHT
9		K	
10		L	
11	1P GAME SELECT	M	2P GAME SELECT
12	COIN SWITCH 1	N	COIN SWITCH 2
13	COIN COUNTER 1	P	COIN COUNTER 2
14	TV. B	R	TV. G
15	SYNC	S	TV. R
16		T	
17	SOUND +	U	SOUND –
18	+12V	V	
19	+5V	W	+5V
20	+5V	X	+5V
21	CRT GND	Y	GND
22	GND	Z	GND

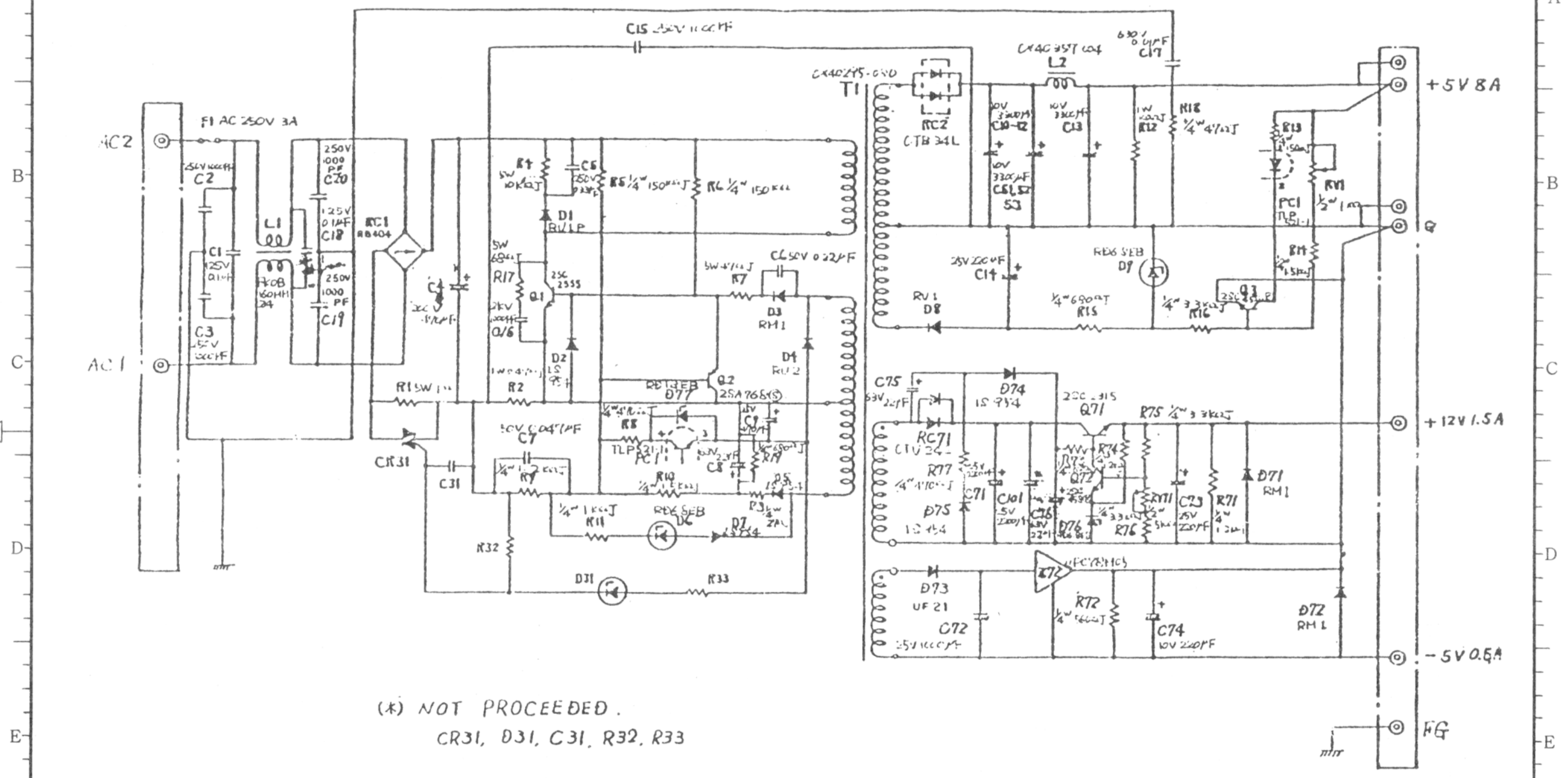
COIN COUNTER CIRCUIT

NOTE: Only Coin Counter #2 is installed at factory.

PCB DFV-01

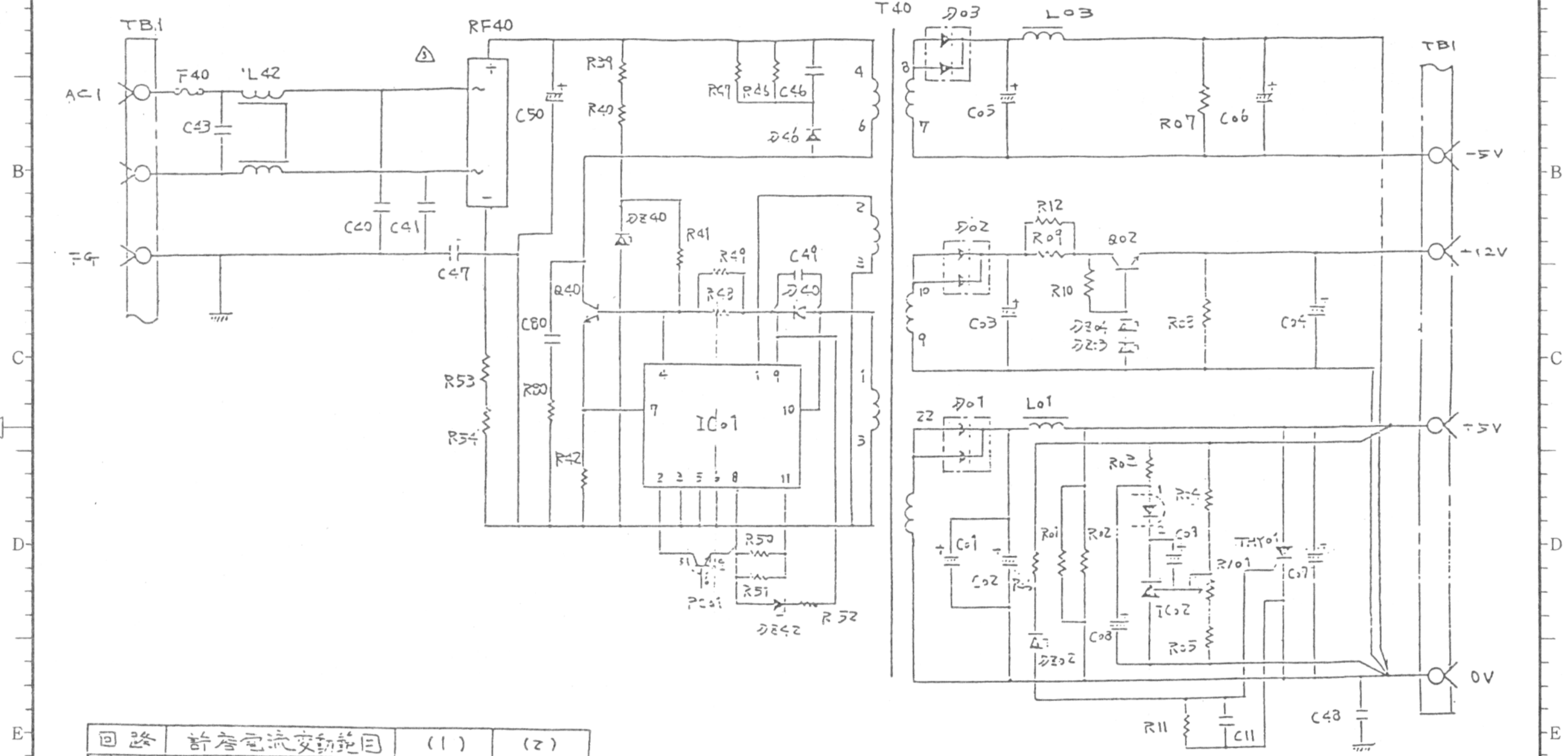


関連番号	仕様，名称



名称	SANKEN SWS-60B POWER SUPPLY
図番	K-10505

関連番号	仕様，名称



回路	許容電流変動範囲	(1)	(2)
+5V	1A ～ 7A	7A	6A
+12V	0.2A ～ 1.5A	1.0A	1.5A
-5V	0.2A ～ 0.5A	0.5A	0.5A

但し出力容量はTotalで50W

C49は実装されません。

名称	KGD23SMKII-3ch POWER SUPPLY
図番	WM-830066

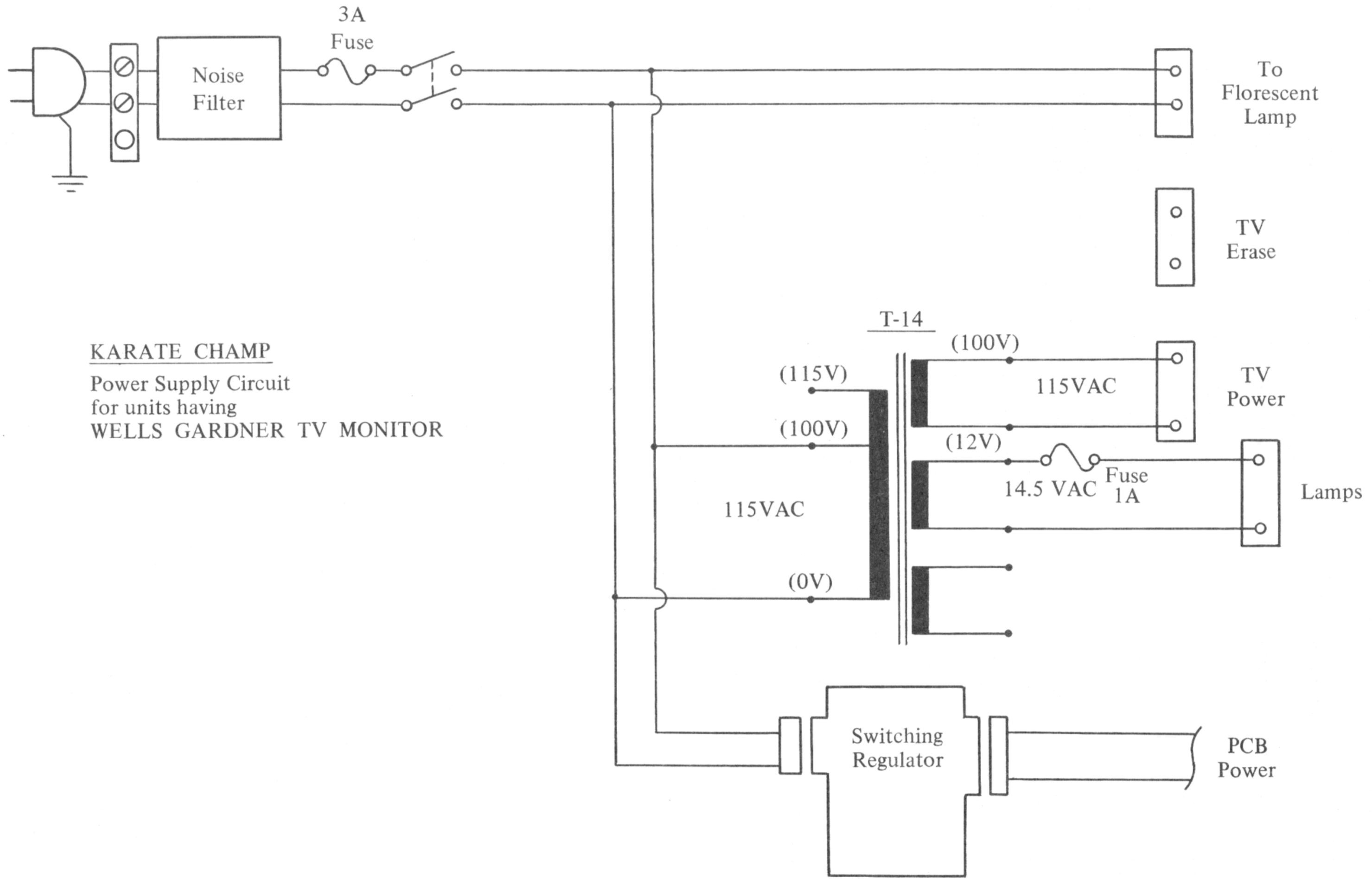
POWER SUPPLY Model KGD23SMK-II

Item	Parts Nos	Description	Specification	Q'ty	Manufacturer
1.	F40	Fuse	GHS 5	1	Nagasawa
2.	T40	Transformer		1	Shindengen
3.	L42	Common Mode Choke	UF327S 602YIRO	1	TDK
4.	Q40	Transistor	2SC3306	1	Toshiba
5.	RF40	Bridge Rectifier	S3WB 40	1	Shindengen
6.	D40	Diode	V19C	1	Hitachi
7.	D46	Diode	V19G	1	Hitachi
8.	DZ40	Zener Diode	HZ16-1	1	Hitachi
9.	IC01	HB IC	RHD01-2	1	Nichi-con
10.	PC01	Photo Coupler	PS 2018	1	Nichiden (NEC)
11.	C40, 41	Ceramic Capacitor	DE7090B 102KVAl 125V 1,000pf	2	Murata
12.	C47	Ceramic Capacitor	– ditto–	1	Murata
13.	C80	Ceramic Capacitor	DE0707B 681K 2KV DC 680p	1	Murata
14.	C42	Film Capacitor			
15.	C43	Film Capacitor	QXM2G 104KT 400V 0.1μf	1	Nichi-con
16.	C46	Film Capacitor	ECQ-E2 104KS 200V 0.1μf	1	Matsushita
17.	C48	Film Capacitor	ECQ-E6 473KZ 400V 0.047μf	1	Matsushita
18.	C50	Electrode Capacitor	LJA2D471 THSCBV 200V 470μf	1	Nichi-con
19.	R37, 40	Resistor	ERG-1SJ 393H	2	Matsushita
20	R47, 46	Resistor	ERG-2SJ 223H	2	Matsushita
21.	R48, 49	Resistor	ERG-3SJ 560H	2	Matsushita
22.	R80	Resistor	ERG-2SJ 330H	1	Matsushita
23.	R42	Metal Plate Resistor	MFC 70 0.22 ohm	1	Fukushima Futaba
24.	R53, 54	Coil Resistor	MNS 05N 1ROJ	2	Micron
25.	R41	Carbon Resistor	1/4W 10K ohm	1	
26.	R50	Carbon Resistor	1/4W 680 ohm	1	
26.	R51	Carbon Resistor	1/4W 12K ohm	1	
28.	R52	Carbon Resistor			
29.	L01	Choke	RD1113A 3J 3uH 10A	1	Hokkoh
30.	L03	Choke	RD810A 3F 3uH 5A	1	Hokkoh
31.	Q02	Transistor	2SD1022 or 2SD1308	1	Shindengen
32.	D01	Diode	ESAC 83-004	1	Fuji Denki
33.	D02	Diode	5CH2M or ESAC25-020	1	Fuji Denki or NEC
34.	D03	Diode	– ditto –	1	Fuji Denki
35.	DZ02	Zener Diode	HZ 6B-2	1	Hitachi
36.	DZ03	Zener Diode	HZ 12B-1L	1	Hitachi
37.	THY01	Thyristor	5PIM or SF5B41	1	NEC or Toshiba
38.	IC02	I.C.	TL431 CLP	1	T.I.
39.	C01, 02	Capacitor	SXA 16VB 2200	2	Nichi-Chemi
40.	C03, 05	Capacitor	CEUSM 1V 471 35V 470μf	2	Nichi-con

Item	Parts Nos	Description	Specification	Q'ty	Manufacturer
41.	C04	Capacitor	CEUSM 1E 331 25V 330μF	1	Nichi-con
42.	C07, 06	Capacitor	CEUSM 1E 331 25V 330μf	2	Nichi-con
43.	C08	Capacitor	CEUSM 1H 010	1	Nichi-con 50V 1μf
44.	C09	Capacitor	CEUSM 1V 4R7 35V 4.7μf	1	Nichi-con
45.	C11	Film Capacitor	TDY1H/2A 104K 50V 0.1μf	1	Nichi-con
46.	R01, 13	Resistor	ERG 3SJ 680H	2	Matsushita 3W 68Ω
47.	R08	Resistor	ERG3SJ 271H	1	Matsushita 3W 270Ω
48.	R09	Resistor	ERG 2SJ R68 2W 0.68 ohm	1	Matsushita
49.	R10	Resistor	ERG 1SJ 271 1W 270 ohm	1	Matsushita
50.	RV01	Volume	EVM38GA 00B52 500 ohm	1	Matsushita
51.	R03	Carbon Resistor	1/4W 68 ohm	1	
52.	R04, 11	Carbon Resistor	1/4W 560 ohm	2	
53.	R05	Carbon Resistor	1/4W 390 ohm	1	
54.	R06	Carbon Resistor	1/4W 15 ohm	1	
55.	R02				
56.		P.C. Board		1	
57.	TB1	Terminal Strip		1	
58.		Terminal	Faston	7	

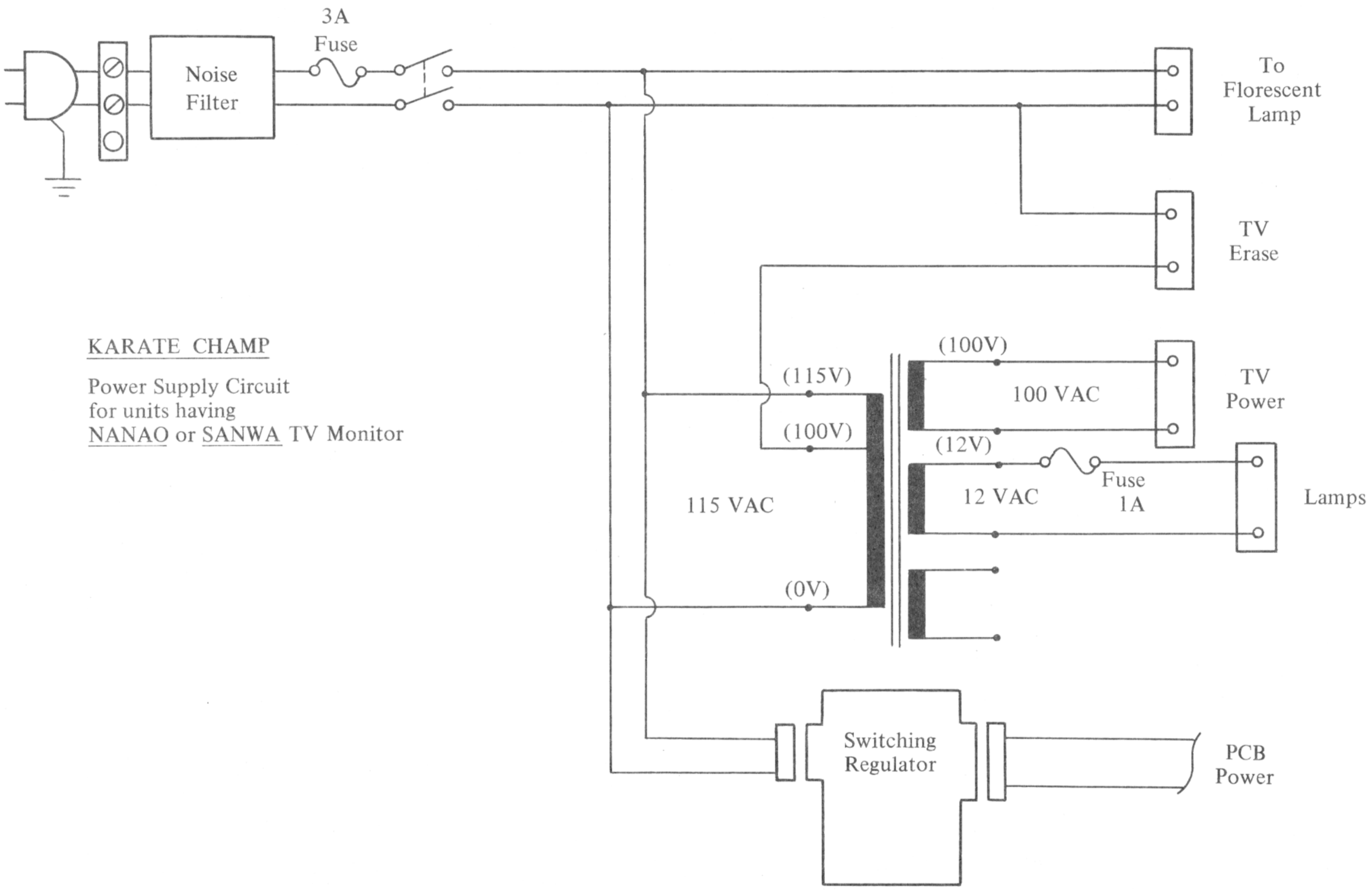
KARATE CHAMP

Power Supply Circuit
for units having
WELLS GARDNER TV MONITOR

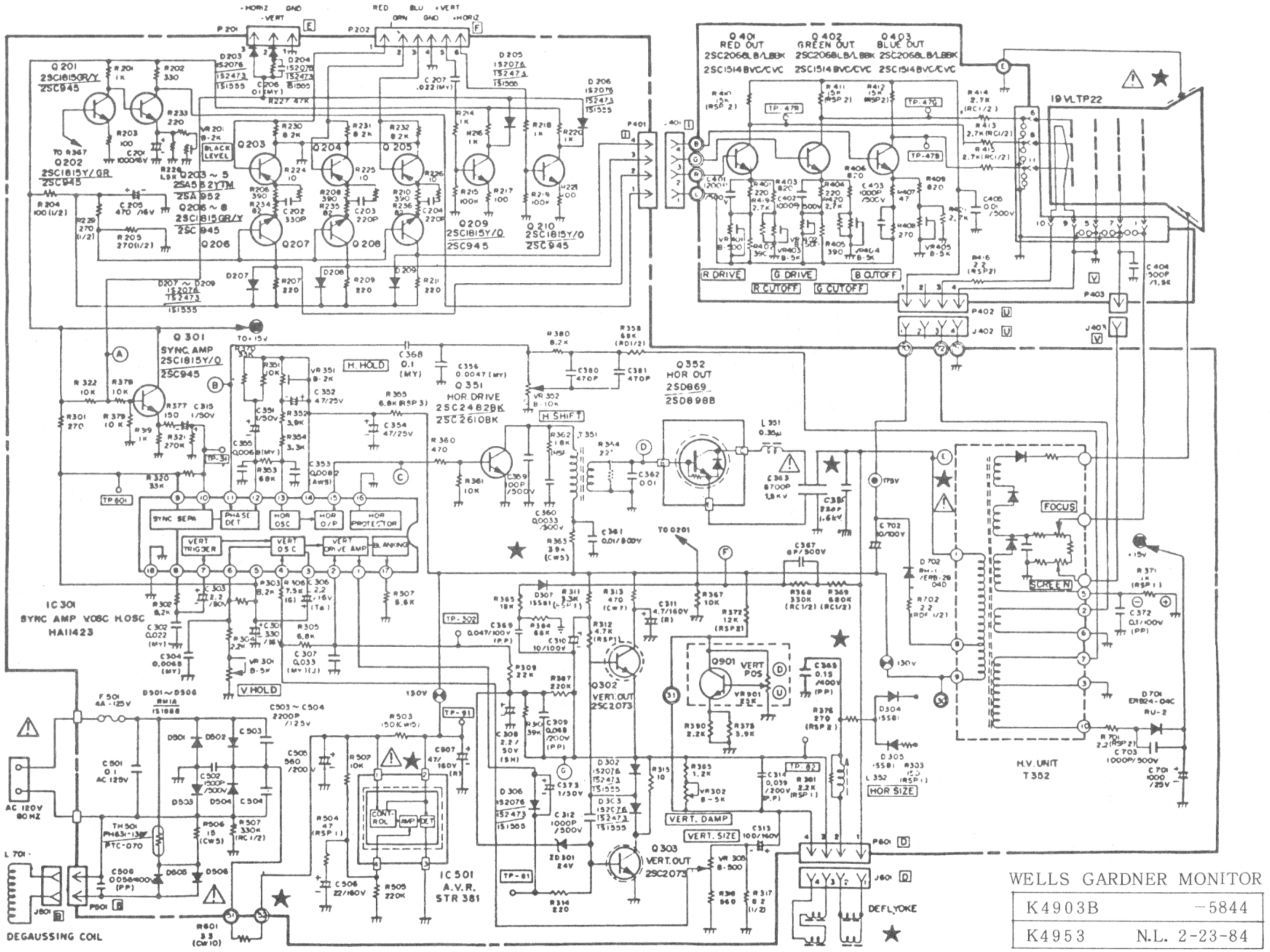


KARATE CHAMP

Power Supply Circuit
for units having
NANAO or SANWA TV Monitor



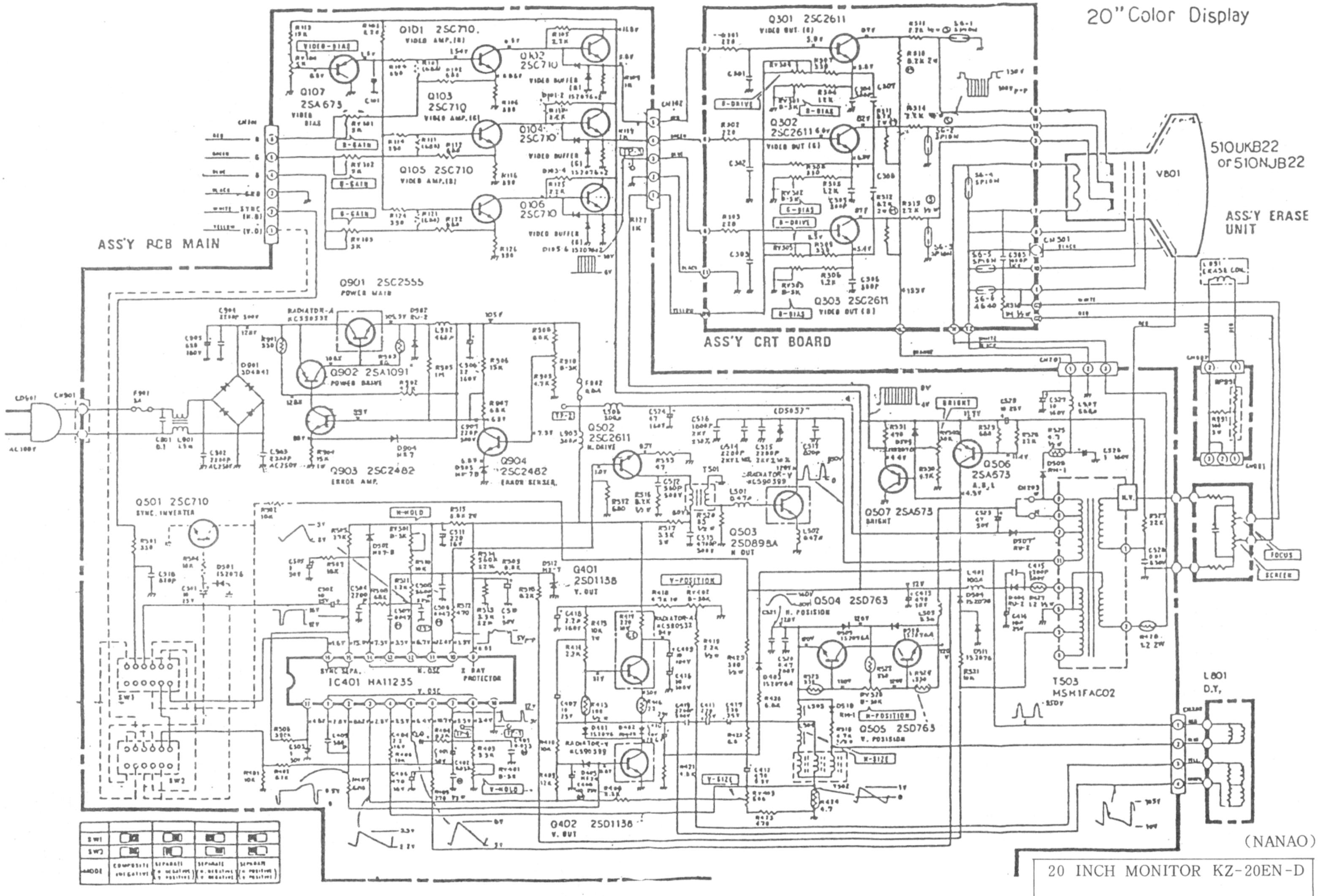
19" COLOR GAME MONITOR SCHEMATIC DIAGRAM



WELLS GARDNER MONITOR

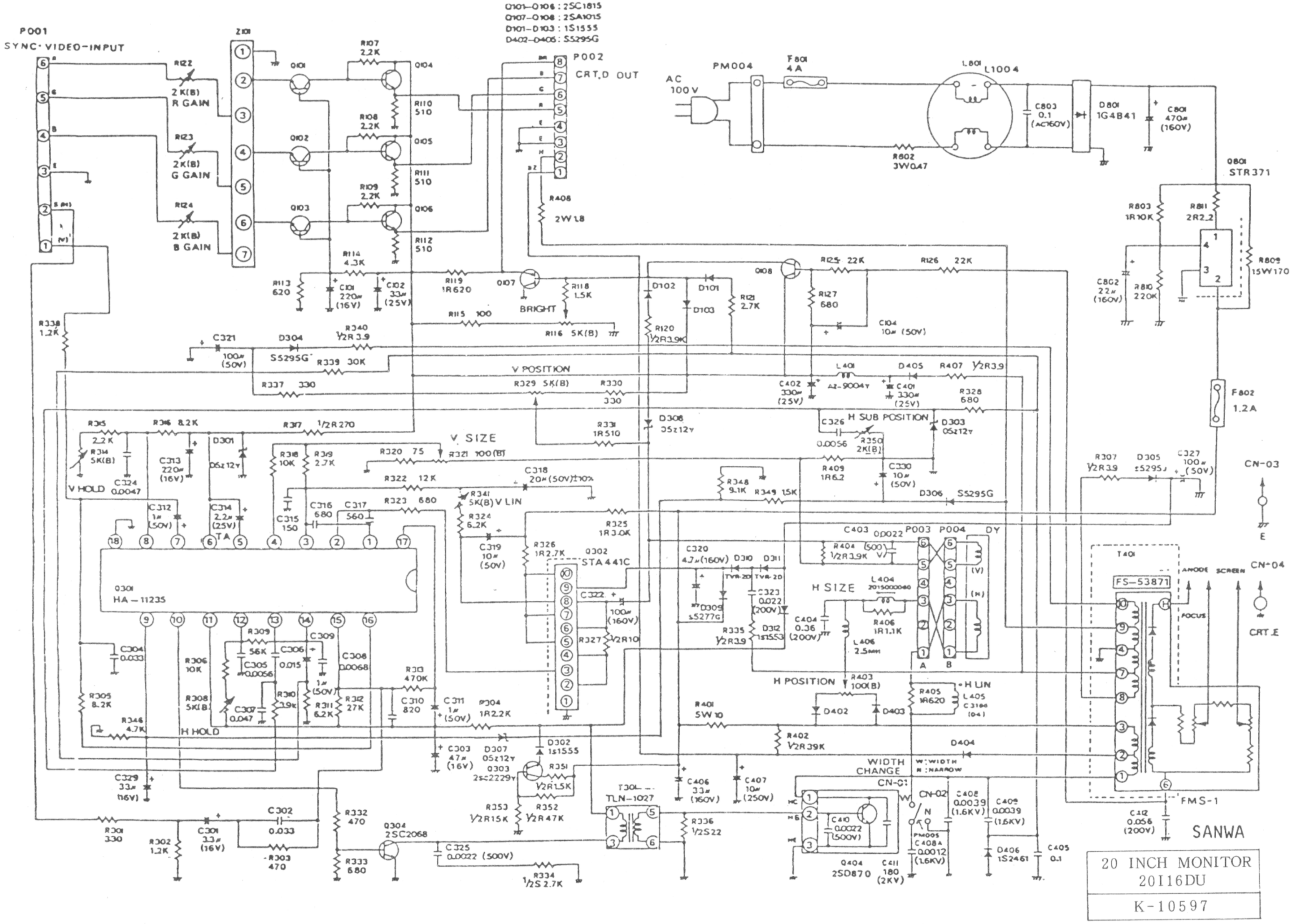
K4903B	-5844
K4953	N.L. 2-23-84

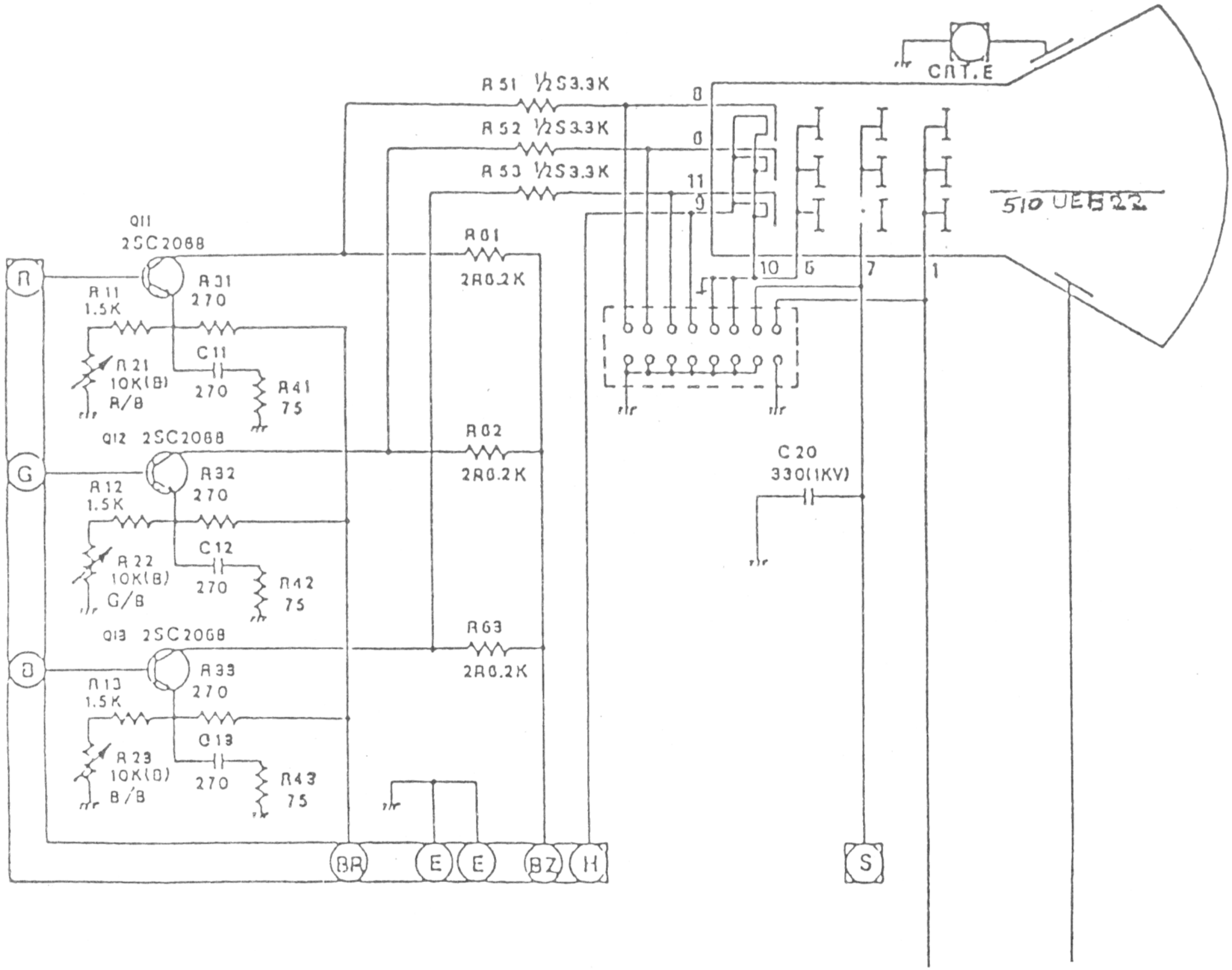
20" Color Display



(NANAO)

20 INCH MONITOR KZ-20EN-D
K-10364


Q101–Q106 : 2SC1815
Q107–Q108 : 2SA1015
D101–D103 : 1S1555
D402–D405 : S5295G
P001
SYNC·VIDEO-INPUT
P002
CRT,D OUT
AC 100V
PM004
F801 4A
L801 L1004
C803 0.1 (AC160V)
D801 1G4B41
C801 470㎌ (160V)
R802 3W0.47
Q801 STR371
R803 1R10K
R811 2R2_2
R809 15W170
C802 22㎌ (160V)
R810 220K
F802 1.2A
CN-03
E
CN-04
CRT,E
R122 2K(B) R GAIN
R123 2K(B) G GAIN
R124 2K(B) B GAIN
R107 2.2K
R108 2.2K
R109 2.2K
R110 510
R111 510
R112 510
R114 4.3K
R113 620
C101 220㎌ (16V)
C102 33㎌ (25V)
R119 1R620
Q107
BRIGHT
R118 1.5K
R115 100
R116 5K(B)
R408 2W 1.8
R338 1.2K
C321 100㎌ (50V)
D304 S5295G
R340 ½R3.9
R339 30K
R337 330
V POSITION
R329 5K(B)
R330 330
R331 1R510
R125 22K
R126 22K
R127 680
C104 10㎌ (50V)
R120 ½R3.9K
R121 2.7K
D102
D101
D103
Q108
C402 330㎌ (25V)
L401 AZ-9004Y
C401 330㎌ (25V)
D405
R407 ½R3.9
R328 680
D308 05Z12Y
C326 0.0056
R350 2K(B)
H SUB POSITION
D303 05Z12Y
R409 1R6.2
C330 10㎌ (50V)
D306 S5295G
R348 9.1K
R349 1.5K
R365 2.2K
R314 5K(B)
V HOLD
C324 0.0047
R366 8.2K
C313 220㎌ (16V)
D301 05Z12Y
R367 1/2R270
V SIZE
R318 10K
R319 2.7K
R320 75
R321 100(B)
R322 12K
C318 20㎌(50V)±10%
R341 5K(B) V LIN
R323 680
C315 150
C316 680
C317 560
R324 6.2K
C319 10㎌ (50V)
R325 1R3.0K
C312 1㎌ (50V)
C314 2.2㎌ (25V)
TA
R326 1R2.7K
Q302 STA441C
Q301 HA-11235
C322 100㎌ (160V)
R327 ½R10
C320 4.7㎌(160V)
D310
D311
TVR-20
TVR-20
D309 S5277G
C323 0.022 (200V)
D312 1S1553
R335 ½R3.9
H SIZE
L404 2015000040
C404 0.36 (200V)
L406 2.5mH
R406 1R1.1K
C403 0.0022
P003
P004
DY
R404 (500) ½R3.9K V
A
B
(V)
(H)
T401
FS-53871
ANODE
SCREEN
FOCUS
R309 56K
C306 0.015
C309 1㎌ (50V)
C308 0.0068
R306 10K
C305 0.0056
R308 5K(B)
C307 0.047
R310 3.9K
R311 6.2K
R312 27K
R313 470K
C310 820
C311 1㎌ (50V)
R304 1R2.2K
H HOLD
R305 8.2K
R346 4.7K
R403 100(B)
H POSITION
D402
D403
R405 1R620
+H LIN
L405 C 3166 (04)
R401 SW 10
R402 ½R3.9K
D404
WIDTH CHANGE
W:WIDTH
N:NARROW
CN-01
CN-02
C303 4.7㎌ (16V)
D307 05Z12Y
Q303 2SC2229Y
D302 1S1555
R351 ½R1.5K
R353 ½R1.5K
R352 ½R4.7K
T301 TLN-1027
C406 33㎌ (160V)
C407 10㎌ (250V)
C329 33㎌ (16V)
C302 0.033
R301 330
R302 1.2K
C301 33㎌ (16V)
R303 470
R332 470
R333 680
Q304 2SC2068
C325 0.0022 (500V)
R334 ½S2.7K
R336 ½S22
Q404 2SD870
C411 180 (2KV)
CN-02
N
PM005
C408A 0.0012 (1.6KV)
C408 0.0039 (1.6KV)
C409 0.0039 (1.6KV)
D406 1S2461
C405 0.1
R307 ½R3.9
D305 S5295J
C327 100㎌ (50V)
C412 0.056 (200V)
FMS-1
SANWA
20 INCH MONITOR
20I16DU
K-10597



CRT.D W/B (PC1002) SANWA

名称 NOI-80 SCHEMATIC DIAGRAM

図番 K-10805

1/12

CFUCLK (1-2E)

*LWAIT (2-4E)

(2-4A) *VRAMRQ

*VRAMBSY

CPUCLK A16

CN1X2

12J

VR LS74 11K CLR

+5V

9H

R1 330Ω

DH LS367

(3-4A) *NAIION

PK LS74 9F CLR

CN1

* VBLK E-2

8H

R2 10K

Σ-80A 12A

WAIT NMI INT BREQ RST Vcc GND

(6-2A) *RST

0.1μ C.C

MREQ IOEB RD WD M1 REFH

12H LS367 1/2

*MRQ (2-5E)

*IORQ (3-2E)

*RD (2-3E) (3-2E)

*WD (2-4E) (3-3E)

DXBC 8C 10A

RA1 10KX8 抵抗

A→B LS245 12C

ABO~15 (2-4E)

220P CC

11H

11E

11C

DBO~7 (2-9E) (3-8E) (4-1E) (6-3E) (7-7E)

抵抗 RA2 10kX8

関連番号	仕様, 名称

関連番号	仕様，名称

(1-5A) *RD
(1-2A) *LWAIT
(1-5A) *WD
(1-5A) AB 0~15
(1-4A) *MREQ
(1-8A) DB 0~7

13H
CN1
A1 *VDAMRD
CN/X3
A2 *VRAMWD
A5 *OBJ WD
A3 *OBJ RQ
*VRAM RQ
(1-2E)

13F
12F LS138
11F
12H
+5V

2764 13D
2764 11D
2764 10D
2764 9D
2764 7D
2764 OR 27128 5D
58725 9A
58725 7A
0.1μ C.C

名称	NOI-80 SCHEMATIC DIAGRAM
図番	K-10805
	2/12

名称 NOI-80 SCHEMATIC DIAGRAM

図番 K-10805

3/12

(1-5A) *IORQ
(1-5A) *RD
(1-5A) *WD
(6-2A) *RST
(1-5A) AB 0~15
DB0~7 (1-8A)
+5V
RA3 10K×8 負荷抵抗
DIPSW1 10C
12J
13J
LS139
LS134
LS259 8C
LS245 9C
B1 CN1
+5

*IN2 (4-8E)
*IN1 (4-8E)
*IN0 (4-8E)
*SUNDRQ (7-6E)
(7-3E)(9-2E) *SUNDRST
NMION (1-3E)
*FLIP

名称 NOI-80 SCHEMATIC DIAGRAM

図番 K-10805

4/12

(1-8A) DB0~7

(11-1A) *IP1 SW1 D
(11-2A) *IP1 SW1 U
(11-2A) *IP1 SW1 L
(11-2A) *IP1 SW1 R

(11-3A) *IP1 SW2 D
(11-3A) *IP1 SW2 U
(11-3A) *IP1 SW2 L
(11-4A) *IP1 SW2 R

(11-4A) *IP2 SW1 D
(11-4A) *IP2 SW1 U
(11-5A) *IP2 SW1 L
(11-5A) *IP2 SW1 R

(11-5A) *IP2 SW2 D
(11-6A) *IP2 SW2 U
(11-6A) *IP2 SW2 L
(11-6A) *IP2 SW2 R

(11-7A) *IP2SEL
(11-7A) *IP1SEL
(11-7A) *ICOIN1
(11-8A) *ICOIN2

RCDM -I1

7F 74367
6F 74367
6F 74367
5F ½ 74367
4F ½ 74367

8H
1k R3
1k R4
C1162 Q1
C1162 Q2
0.1μ C3 C.C
0.1μ C4 C.C

ICINCU2 (11-8E)
ICINCU1 (11-8E)

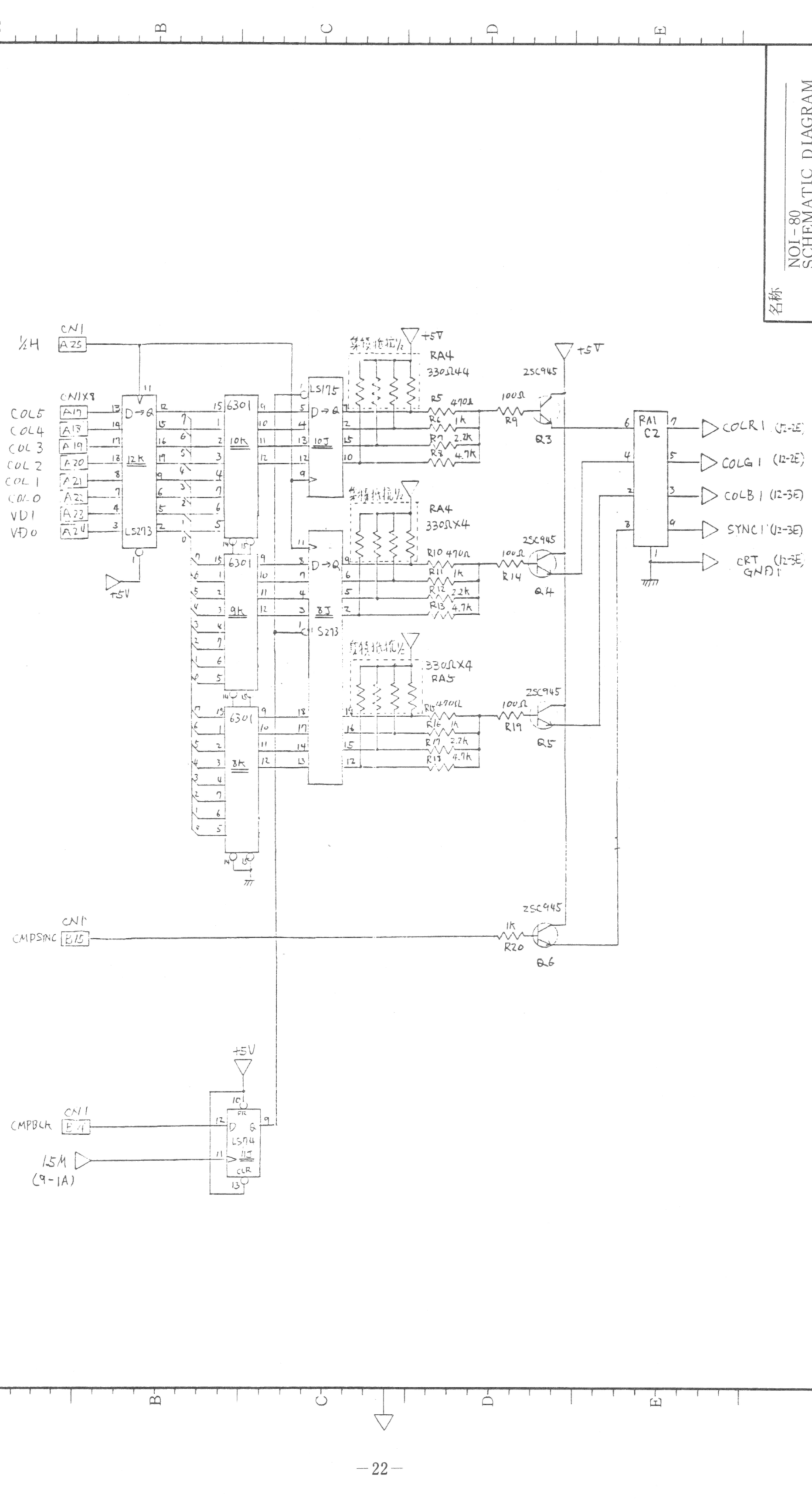
(3-2A) *IN0
(3-2A) *IN1
(3-2A) *IN2

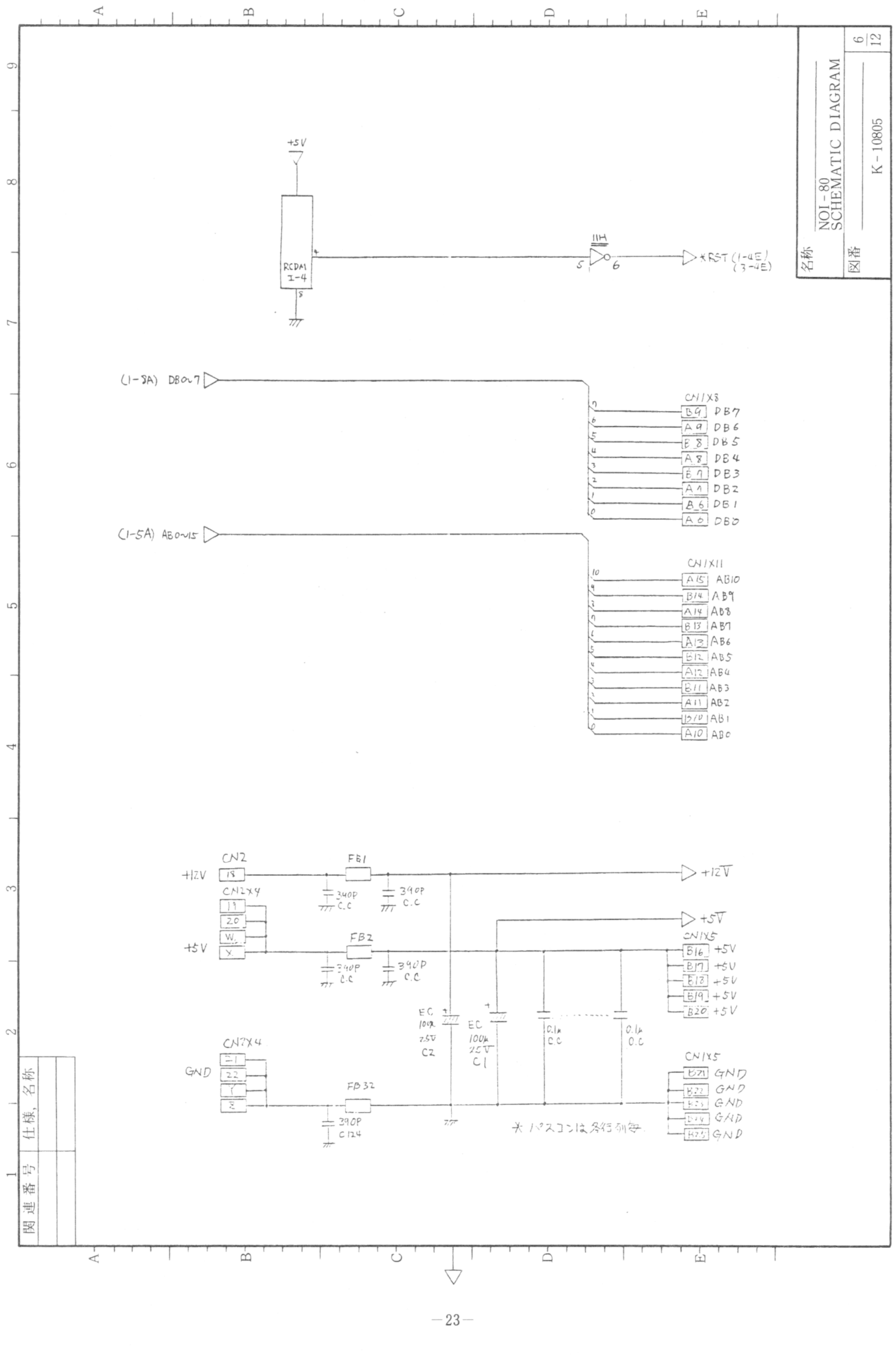
関連番号 | 仕様, 名称

NOI-80
SCHEMATIC DIAGRAM

K - 10805

5/12




名称
NOI-80
SCHEMATIC DIAGRAM
図番
K-10805
6/12
+5V
RCDM
I-4
5
6
*RST (1-6B)
(3-4B)
(1-8A) DB0~7
CN1X8
B9 DB7
A9 DB6
B8 DB5
A8 DB4
B7 DB3
A7 DB2
B6 DB1
A6 DB0
(1-5A) AB0~15
CN1X11
A15 AB10
B14 AB9
A14 AB8
B13 AB7
A13 AB6
B12 AB5
A12 AB4
B11 AB3
A11 AB2
B10 AB1
A10 AB0
CN2
+12V
18
CN2X4
19
20
W
X
+5V
FB1
FB2
340P C.C
+12V
+5V
CN1X5
B16 +5V
B17 +5V
B18 +5V
B19 +5V
B20 +5V
EC 10μ 25V C2
EC 100μ 25V C1
0.1μ C.C
CN7X4
GND
21
22
Y
Z
FB32
390P C124
*パスコンは各行列毎
CN1X5
B21 GND
B22 GND
B23 GND
B24 GND
B25 GND
関連番号
仕様, 名称

名称 NOI-80 SCHEMATIC DIAGRAM

図番 K-10805

7/12

(1-1A) CPUCLK

LS367 9F

R21 330Ω

+5V

A6 8C

WAIT

(9-6A) *SUNDNMI

NMI

(3-0A) *SUNDRST

RST

INT

+5V

Vcc

BRQ

0.1μ C.C

GND

A15 A0 D7 D0

A↔B 6C

LS245 14F

SAB0~15 (8-3E)

SDB0~7 (9-2E) (8-9E)

MRQ IORQ WD RD

8F LS367

*SMRQ (8-5E)

*SIORQ (8-4E)

*SWD (8-6E)

*SRD (8-6E)

+5V

PR D CLR Q LS74 9F

(8-4A) *SUNDRD

*SUNDRQ (3-3A)

D→Q 7C LS374

(1-8A) DB0~7

関連番号 | 仕様, 名称

名称 NOI-80 SCHEMATIC DIAGRAM

図番 K-10805

8/12

*SUND WD (9-7E)

*ADPCM WD (9-6E)

+5V

(7-2A) SAB0~15

LS139 2C

BC12 (9-1E)

DIR2 (9-1E)

BC11 (9-2E)

DIR1 (9-2E)

(7-5A) *SIORQ

10H

LS139 1/2 3C

XSUND RD (7-6E)

(7-5A) *SMRQ.

LS139 1/2 3C

(7-5A) *SWD

(7-5A) *SRD

2764 4D

2764 2D

2764 1D

58725 4A

+5V

0.1μ C.C

(7-4A) SDB0~7

関連番号	仕様, 名称

名称 NOI-80 SCHEMATIC DIAGRAM

図番 K-10805

9/12

15M (5.7E)

(8-3B) DIR2
(8-2B) BC12
(8-3B) DIR1
(8-3B) BC11

(3-4A) *SCNDRST

PSGCLK B5 CN1

(7-4A) SDB0~7

8910 3A

3910 1A

+5V

0.1μ C.C

PSGOUT1 (10-2E)

PSGOUT2 (10-3E)

ADPCMDB (10-6E)

(8-2B) *ADPCMWD

(10-7A) *VCK

(8-2A) *SOND.WD

LS273 D→Q 5C

LS157 4C

LS175 10F

LS74 11J

LS74 9H

*SNMI (7-3E)

ADPRST (10-7E)

関連番号	仕様, 名称

NOI-80
SCHEMATIC DIAGRAM

K-10805

10/12

名称

図番

(9-3A) PSGOUT1

(9-4A) PSGOUT2

+12V

0.068μ C11

R24 47k

MC

R27 47k

1.0μ C9

0.068μ

MC C12

1/2 4558

2H

R22 1K

R23 1K

22K R26

47K R25

0.68μ E.C C10

MC01μ x3 C13

100k R29

C14

R30 100k

C15

R31

R28 1K

10k R32

220P C16 CC

1/2 4558

2H

8.2k R34

C18 0.01μ M.C

+12V

C17 R33 2.2k

10μ E.C

6

5

4558

7

4

2F

+12V

C19 R35 4.7K

10μF T.C

10k R36

10k VR1

EC C22

22μF 16V

TDA 2073

C120 1000μF E.C 16V

R37 2200Ω

C25 0.1μF M.C

R33 2.2Ω

C123 470μF 16V

R39 1Ω

C122 0.1μ CC

FB30

340P C.C C26

34P C.C C27

17

FB31

U

39P C.C C121

+5V

抵抗網 6/9 10KX6 RA6

ADPCM DATA (9-5A)

MS 5205

3E

+5V

0.1μ CC

CN1

A4

ADPCM CLK

+5V

330Ω

2SA733

*VCK (4-6E)

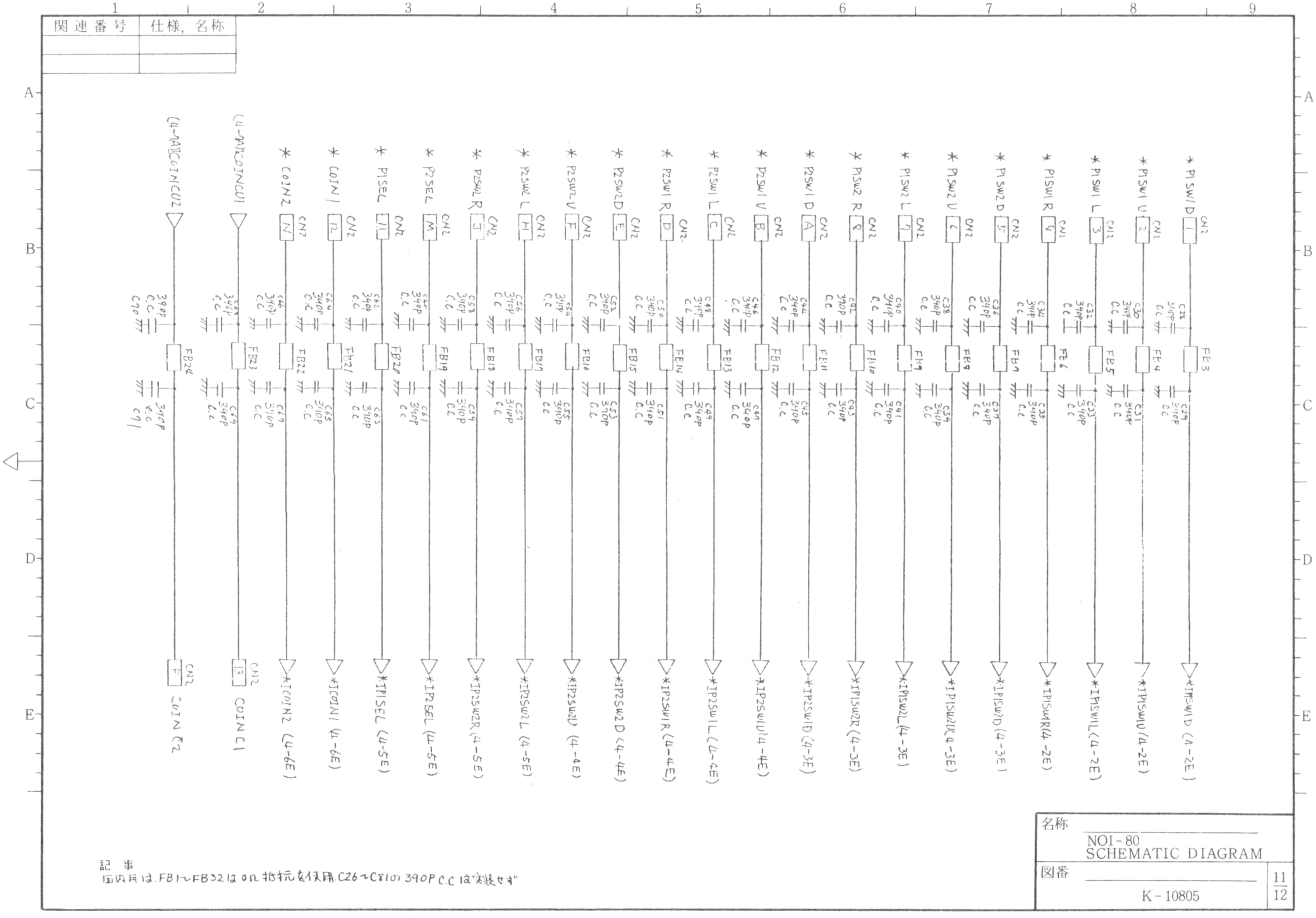
ADP RST (9-11A)

※接続面はグランドプレーンにすること。

関連番号

仕様, 名称

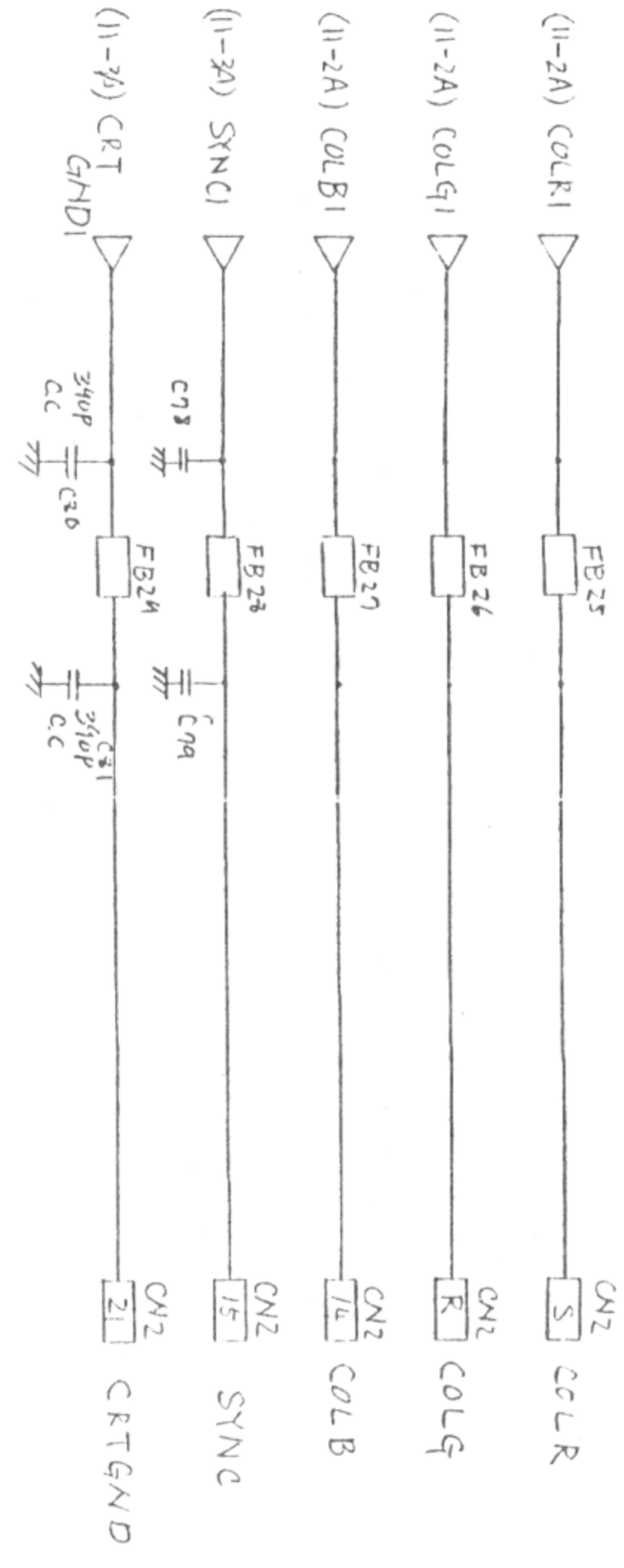
関連番号	仕様，名称



記事
国内用は，FB1～FB32は0Ω抵抗を使用．C26～C81の390PC.Cは実装せず

名称	NOI-80 SCHEMATIC DIAGRAM	
図番	K-10805	11/12

関連番号	仕様, 名称



記事 ・回路図はFB1～FB3とはCR抵抗を使用 C26～C81の330PCCは実装せず
・C72～C79のコンデンサーは実装せず.

名称	NOI-80 SCHEMATIC DIAGRAM
図番	K-10805
	12/12

NRI-80
SCHEMATIC DIAGRAM

名称

図番 K-10806 1/11

※はSTD 04使用

R2 1.5K　X1 12M　R1 1.5K　R3 33K　180P　C39　C40 220P

*Ø34 (10-8E)

*Ø30 (9-1E)

+5V

4J LS163　4H LS163　3H LS163　3J

*½H (10-7E)(7-7E)(6-1E)(5-1E)

½H (3-6E)(10-8E)(7-1E)

CN1 X3

A25 ½H

A16 CPUCLK

B5 PSG CLK

A4 ADPCMCLK

HCLK0~7 (2-1E)(3-2E)(4-2E)(6-2E)(7-6E)

256H (2-1E)(8-7E)(3-3E)

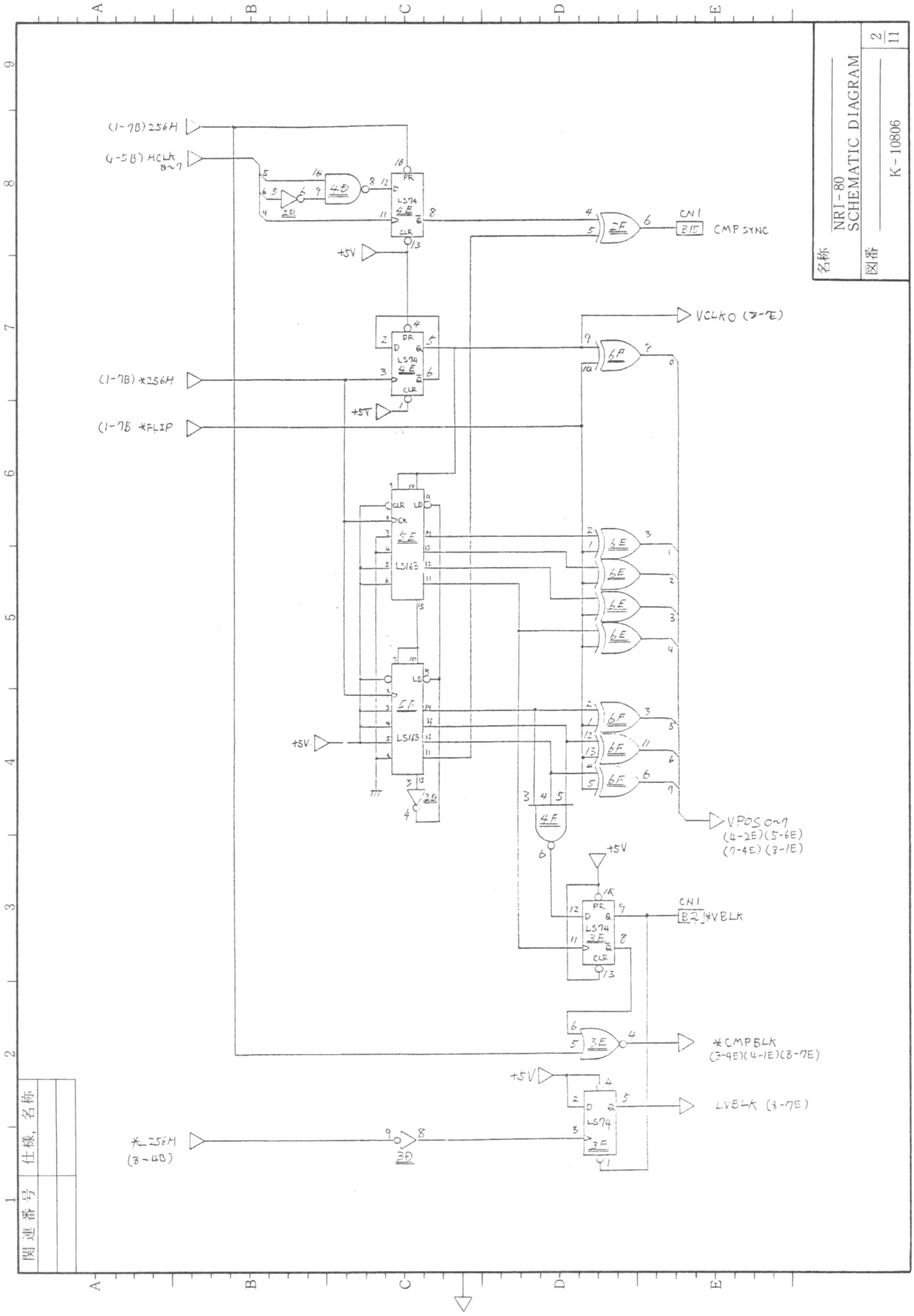
*256H (2-3E)(3-6E)(7-2E)(3-6E)

CN1

*FLIP 21

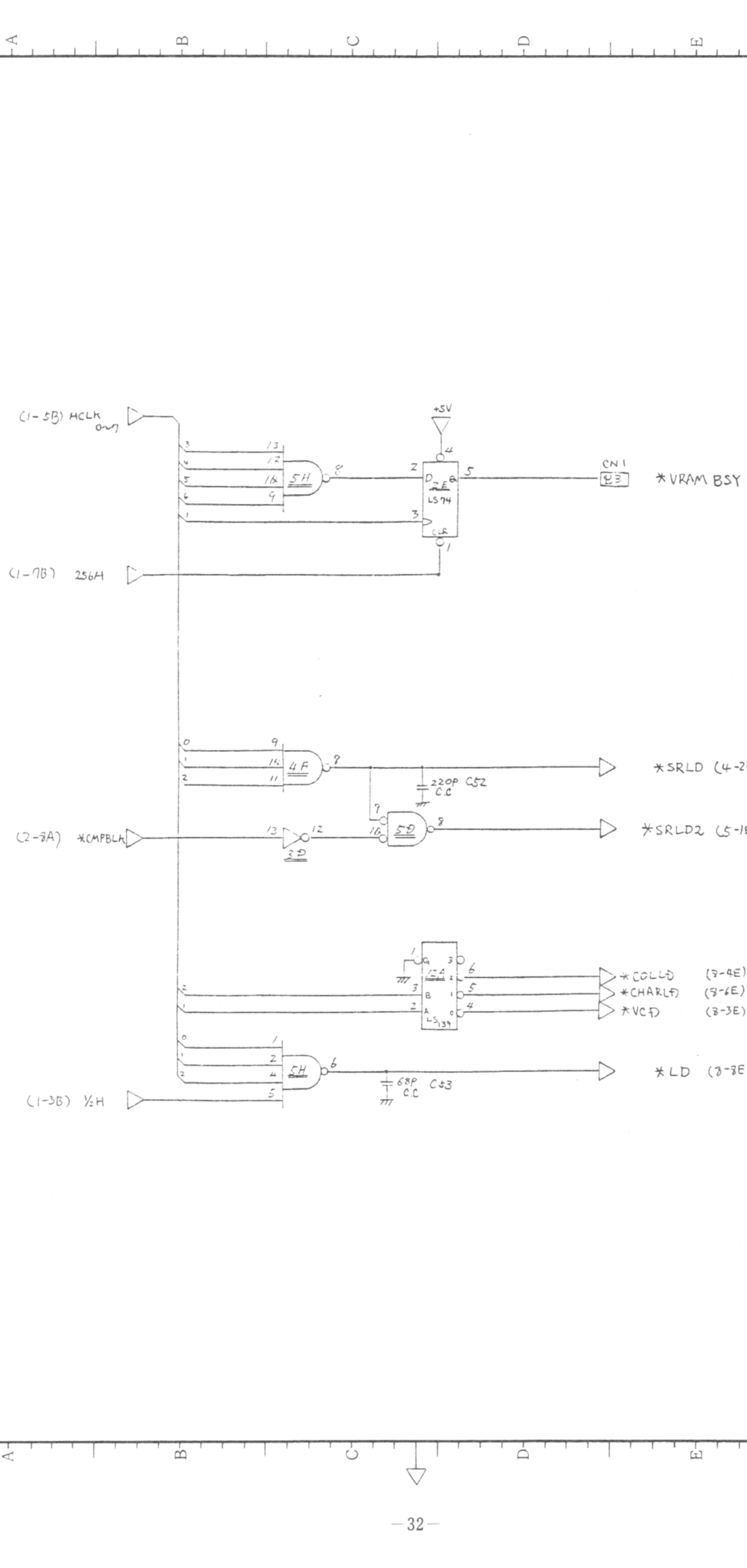
*FLIP (2-3E)(8-7E)

関連番号 | 仕様，名称


NRI-80
SCHEMATIC DIAGRAM
名称
図番
K-10806
2/11
(1-7B) 256H
(4-5B) HCLK 8~7
(1-7B) *256H
(1-7B *FLIP
+5V
LS74
4E
4D
2D
LS163
5F
3D
4F
6F
5E
6E
3E
CLR
LD
CK
PR
CN1
B15
CMP SYNC
VCLKO (3-7E)
VPOS on 7
(4-2E)(5-6E)
(7-4E)(8-1E)
B2
*VBLK
*CMPBLK
(3-4E)(4-1E)(8-7E)
LVBLK (8-7E)
*256H
(8-4B)
関連番号
仕様, 名称

名称 NRI-80 SCHEMATIC DIAGRAM

図番 K-10806 3/11



関連番号	仕様，名称

名称 NRI-80 SCHEMATIC DIAGRAM

図番 K-10806 4/11

(2-xA) *CMPBLK
(2-4B) *SRLD
(1-5B) HCLK
(7-6A) VPOS
(11-3B) AB0~10
*VRAM WD
*VRAM RD
(11-1B) DB0~7
MAPNOL (5-6E)
MAPCOL (10-3E)
MAPNOH (5-2E)
+5V
LS157
LS273
LS174
LS245
S8725
8K
12K
9K
12J
11J
10J
9J
CN1
A2
A1

関連番号	仕様，名称

名称 NRI-80 SCHEMATIC DIAGRAM

図番 K-10806

5/11

(1-3B)*½H

(3-4B)*SRLD2

(4-4B)MAPNOH

MAPNOL (4-2B)

VPOS 0~7 (2-6A)

2764 1K

2764 3K

2764 5K

2764 6K

LS166 1J

LS166 5J

+5V

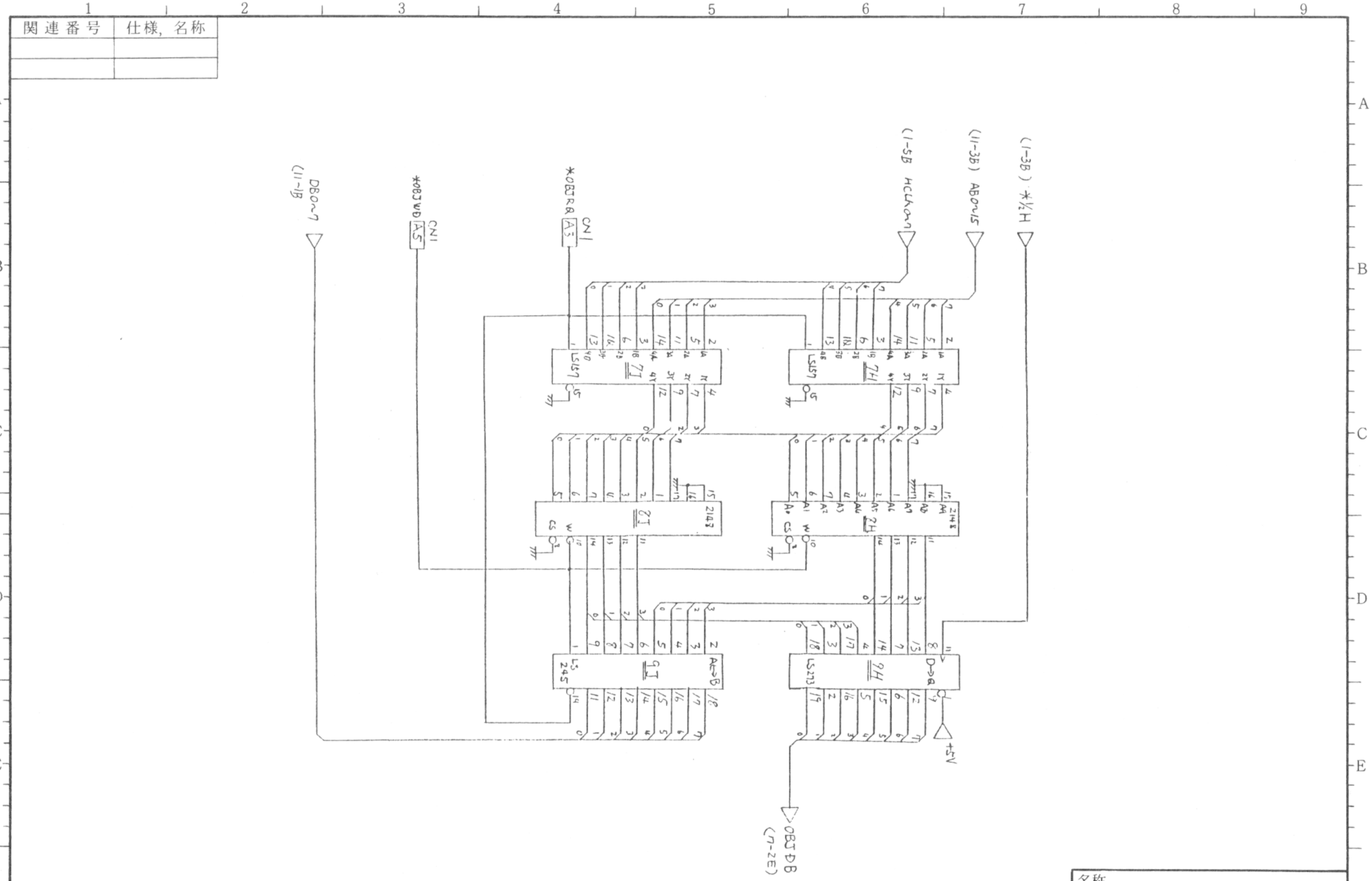
C.C 0.1μ

MAPVD0 (10-2E)

MAPVD1 (10-2E)

関連番号	仕様, 名称

関連番号	仕様，名称



名称	NRI-80 SCHEMATIC DIAGRAM	
図番	K-10806	6/11

名称 NRI-80 SCHEMATIC DIAGRAM

図番 K-10806

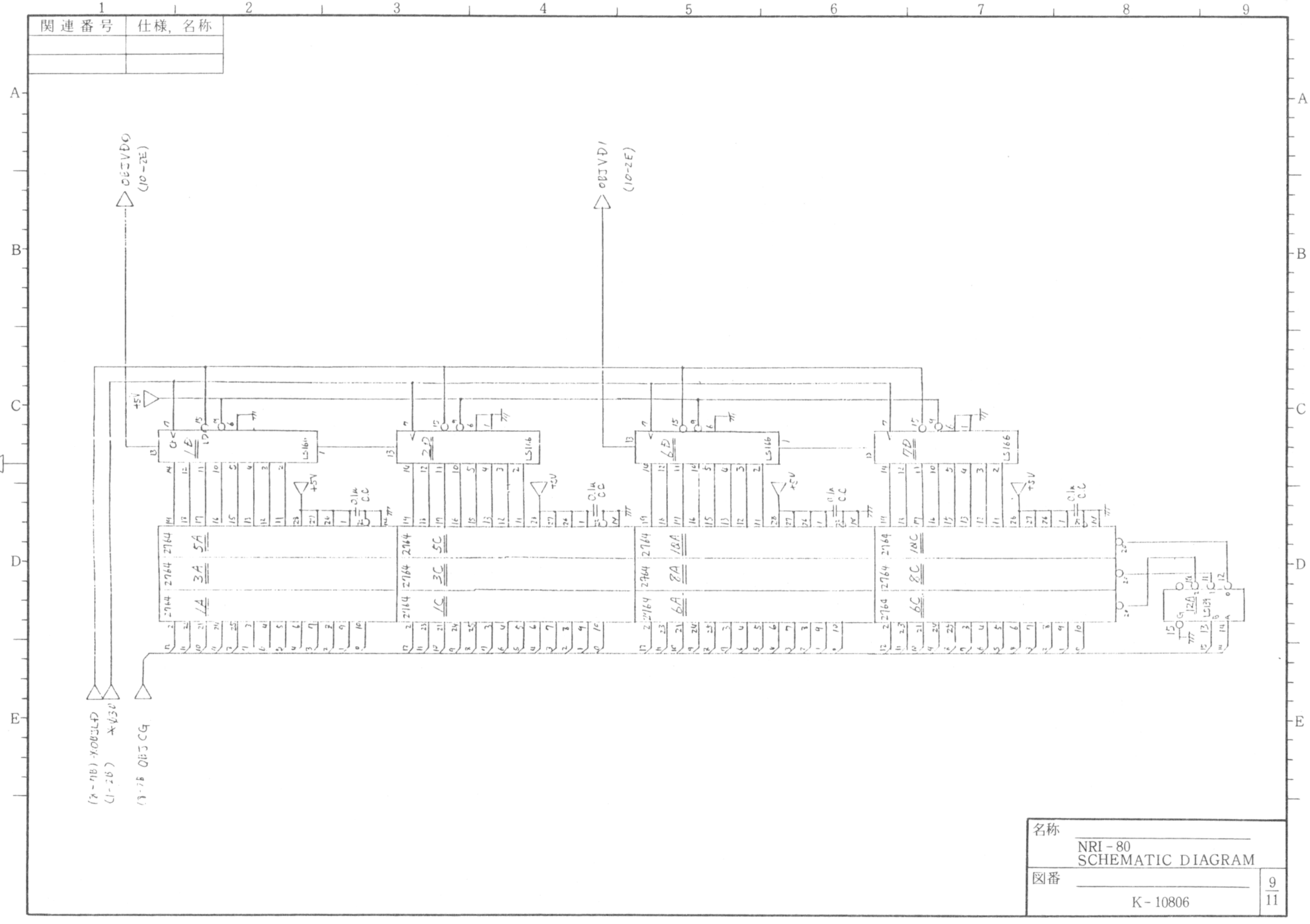
7/11

(1-3B) ½H
(J-7B)*256H
(6-5A) OBJDB
(7-6A) VPOSCNT
(1-3B)HCLKCNT
(3-6A) *LD
(1-3B) *½H
+5V
*LDB (10-5E)
*CLRB (10-7E)
LOBJDB (8~7E) (10-6E)
(8-2E OBJEND
RA1 基板抵抗 470Ω×9
RA2 基板抵抗 470Ω×9
0.1μ CC

(2-6A) VPOSan7
(7-7A) ∠OBJDB
(3-5B) *VCD
(3-5B) *COLLD
(3-5B) *CHARLD
(1-7B) *256H
(1-7B) *FLIP
(2-2B) VCLKD
(2-2A) *CMPBLK
(2-7A) LVELA
(1-7A) *OBJEND
(1-7B) 256H
(3-6B) *LD
+5V
13A LS283
13B LS175
11A LS08
11C LS273
12C LS273
12B LS273
3E LS32
6J LS08
11B LS109
5D LS08
OBJCOL0~3 (10-3E)
CN1 E4 CMPBLK
*LFILP (10-8E)
*L256H (2-8E)(10-1E)
*OBJ LD (9-1E)

関連番号	仕様, 名称

関連番号	仕様, 名称


1
2
3
4
5
6
7
8
9
A
B
C
D
E
(10-ZE) OBIVD0
(10-ZE) OBIVD1
(8-DB) XOBUSD
(1-ZB) ホUSD
(8-DB) OBUSCG
+5V
0.1μ C.C
2764 5A
2764 3A
2764 1A
2764 5C
2764 3C
2764 1C
2764 6A
2764 4A
2764 2A
2764 6C
2764 4C
2764 2C
LS161
LS166
120Ω
1/4W
名称
NRI-80
SCHEMATIC DIAGRAM
図番
K-10806
9/11

NRI-80
SCHEMATIC DIAGRAM

名称

図番 K-10806

10/11

(8-4B)*L25614
(9-1A) OEJVD0
(5-2B) MAPVD0
(9-4A) OEJVD1
(5-5B) MAPVD1
(8-5B) OBJCOL
(4-3B) MAPCOL
(7-8A) LOBJDB
*LDB (3-6B)
(3-6B) *CLRB
*½H (1-3B)
Φ34 (1-2B)
LFLIP (8-4B)
½H (1-3A)

3E
93422
12H
13H
13J
13E
LS164
LS153
LS273
10H
11F
11H
5D
4D
13F
12F
0.1u C.C
220P C.C
+5V
CN1X4
A24 VD0
A23 VD1
A22 COL0
A21 COL1
A20 COL2
A19 COL3
A18 COL4
A17 COL5

関連番号
仕様, 名称

名称 NRI-80 SCHEMATIC DIAGRAM

図番 K-10806

11/11

CN1X8
DB7 B9
DB6 A9
DB5 B8
DB4 A8
DB3 B7
DB2 A7
DB1 B6
DB0 A6

→ DE027 (4-6E)(6-6E)

CN1X11
AB10 A15
AB9 B14
AB8 A14
AB7 B13
AB6 A13
AB5 B12
AB4 A12
AB3 B11
AB2 A11
AB1 B10
AB0 A10

→ AB0～10 (4-5E)(6-2E)

CN1X5
+5V B16 B17 B18 B19 B20

C51 16V/μ E.C 25V
0.1μCC
0.1μC.C

→ +5V

CN1X5
GND B21 B22 B23 B24 B25

＊バスコンは各行列毎.

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